

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Smith et al.

Serial No.: 10/775,718

Filed: February 09, 2004

Cust. No.: 33123

For: **METHOD AND APPARATUS FOR
SELF-REFERENCED WAFER STAGE
POSITIONAL ERROR MAPPING**

Examiner: Koyama, Kumiko

Art Unit: 2876

CERTIFICATE OF FACSIMILE
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UNDER 37 CFR 1.6(d)I hereby certify that this paper and the
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RESPONSE TO FINAL OFFICE ACTION AND
REQUEST FOR RECONSIDERATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action mailed December 05, 2006 in connection with the above-identified application, Applicant requests entry of this response and reconsideration of the application. A Notice of Appeal and request for pre-appeal conference are filed herewith.

A complete listing of all claims is presented which begins on Page 2 of this document.

Remarks begin on Page 6 of this document.

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